Abstract

A method and a device are described for switching between at least two operating modes
(SM, LM) of a processor unit (100, 101) including at least two execution units (ALUA,
ALUB) for running programs (P1, P2, P3), at least one identifier (K2) being assigned to at
least the programs (P1, P2, P3) which differentiates between the at least two operating modes
(SM, LM), and switching between the operating modes is performed as a function of the
identifier (K1-K4, KB) such that the processor unit (100, 101) runs the programs (P1, P2, P3)
according to the assigned operating mode.